IN THE UNITED STATES PATENT AND TRADEMARK OFFICE CENTRAL

Applicant:

Lehmann, et al.

Group Art Unit: 2811

Serial No.:

09/751,474

12/28/2000

Examiner:

TBD

Docket No.:

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Filed: Entitled:

Area Efficient Stacking of Antifuses in Semiconductor Device

Associate Power of Attorney

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

I hereby appoint as associate attorneys of record all attorneys and agents at Customer No. 25962.

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Respectfully submitted,

Attorney of Record